



AF ZMW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Confirmation No.: 5320

USHIYAMA et al. Examiner: T. Le

Serial No.: 10/694,997 Art Unit: 2818

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For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING REDUCED  
CROSS-TALK NOISE

RESPONSE UNDER 37 C.F.R. § 1.116

BOX AF  
Commissioner for Patents  
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June 15, 2005

Sir:

please enter papers dated  
6/15/05

*Thru  
T. Le*

This paper is filed in response to the Office Action mailed March 17, 2005, in  
the above-captioned patent application. In the prosecution of the above-captioned  
patent application, please consider the following: